

Customer Name	<b>Standard Specification</b>	FUJITSU MEDIA DEVICES LIMITED	
System	Cellular Rx (50/200ohms)	DATE	Dec.26, 2003
FMD Part Number	FAR-F5EB-881M50-B28Z	Version 1.0a	

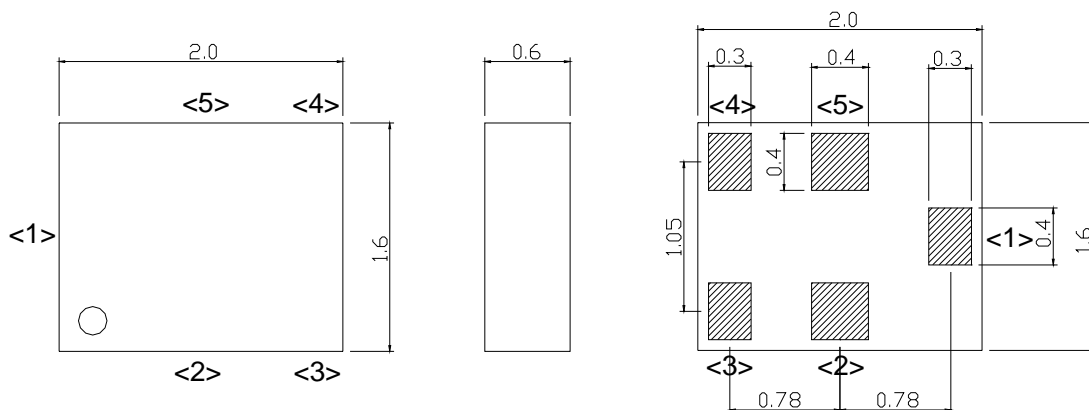
**Table 1. Electrical specifications**

Passband:869 ~ 894 MHz						
Item	Condition	Specification			Unit	Remarks
		Min.	Typ.	Max.		
Insertion Loss	869~894 MHz	-	1.5	2.3	dB	
Ripple	869~894 MHz	-	0.6	1.2	dB	
Absolute attenuation	DC~824 MHz	45	60	-	dB	
	824~849 MHz	35	41	-	dB	
	914~960 MHz	20	26	-	dB	
	960~2000 MHz	40	55	-	dB	
	2000~6000 MHz	35	48	-	dB	
VSWR(Input)	869~894 MHz	-	1.6	2.0	-	
VSWR(Output)	869~894 MHz	-	1.7	2.0	-	
Amplitude balance ( $ S_{21}/S_{31} $ )	869~894 MHz	-0.8	-0.1/+0.2	+0.8	dB	
Phase balance ( $(\phi_{S21}-\phi_{S31})+180$ )	869~894 MHz	-8	-0/+2	+8	deg.	
Input Impedance	Unbalanced	50			Ohm	
Output Impedance	Balance	200			Ohm	
Operating Temperature		-30 ~ +85			°C	
Device size		2.0typ.x1.6typ.x0.6max.			mm	

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### Dimensions

Device size: 2.0typ. x 1.6typ. x 0.6max



Unit: mm

### Pin Configuration

Pin No.	Symbol	Function
1	IN	Unbalanced pin
2	GND	Ground
3	OUT	Balanced pin
4	OUT	Balanced pin
5	GND	Ground

### Evaluation Circuit

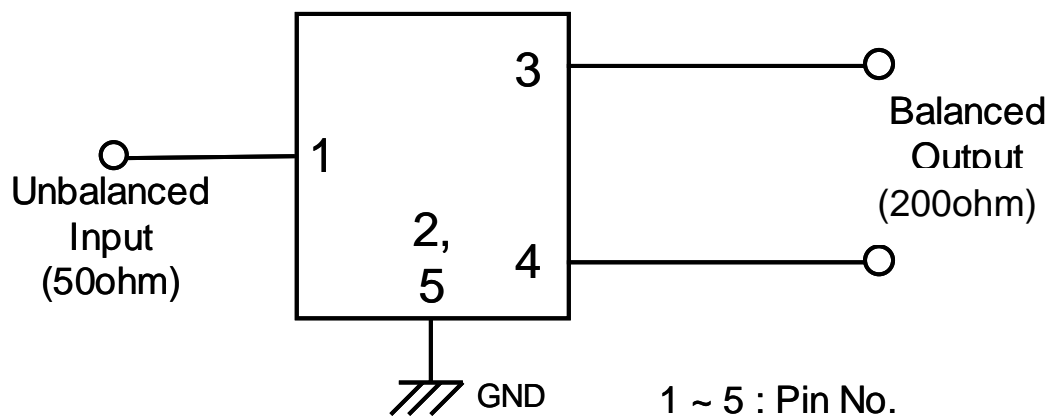


Table 1.Target specifications

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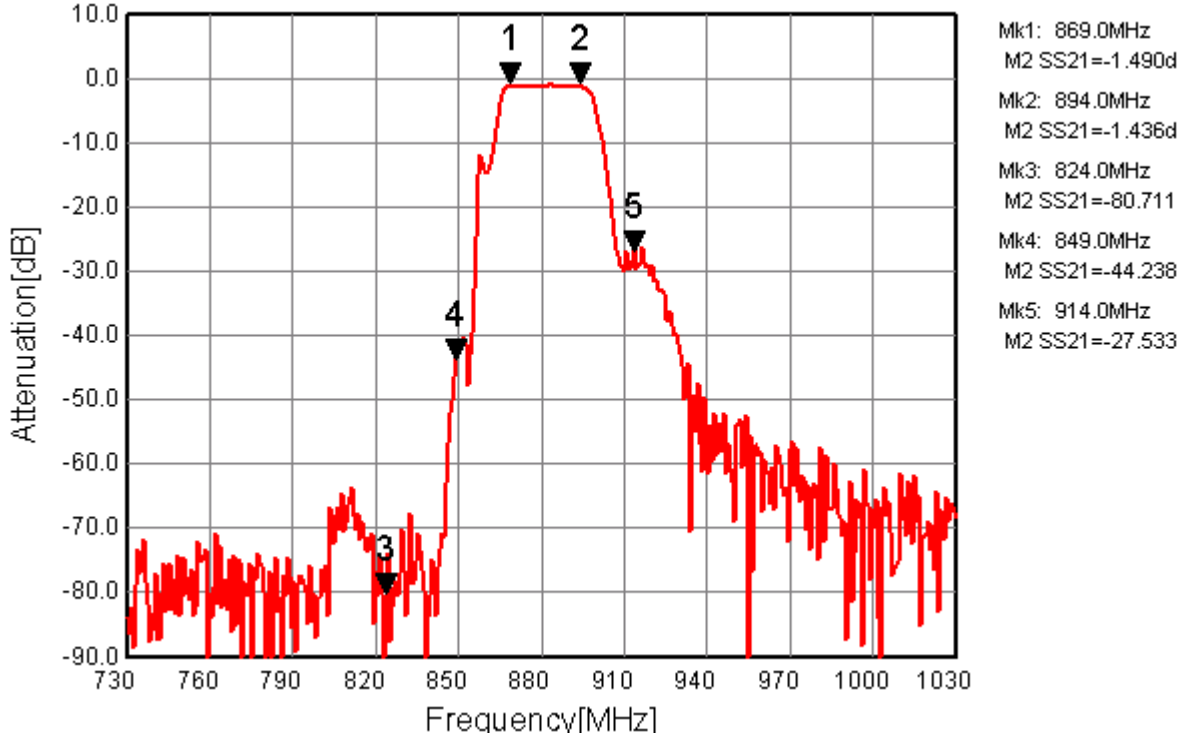


Fig.1 Pass-band Characteristics

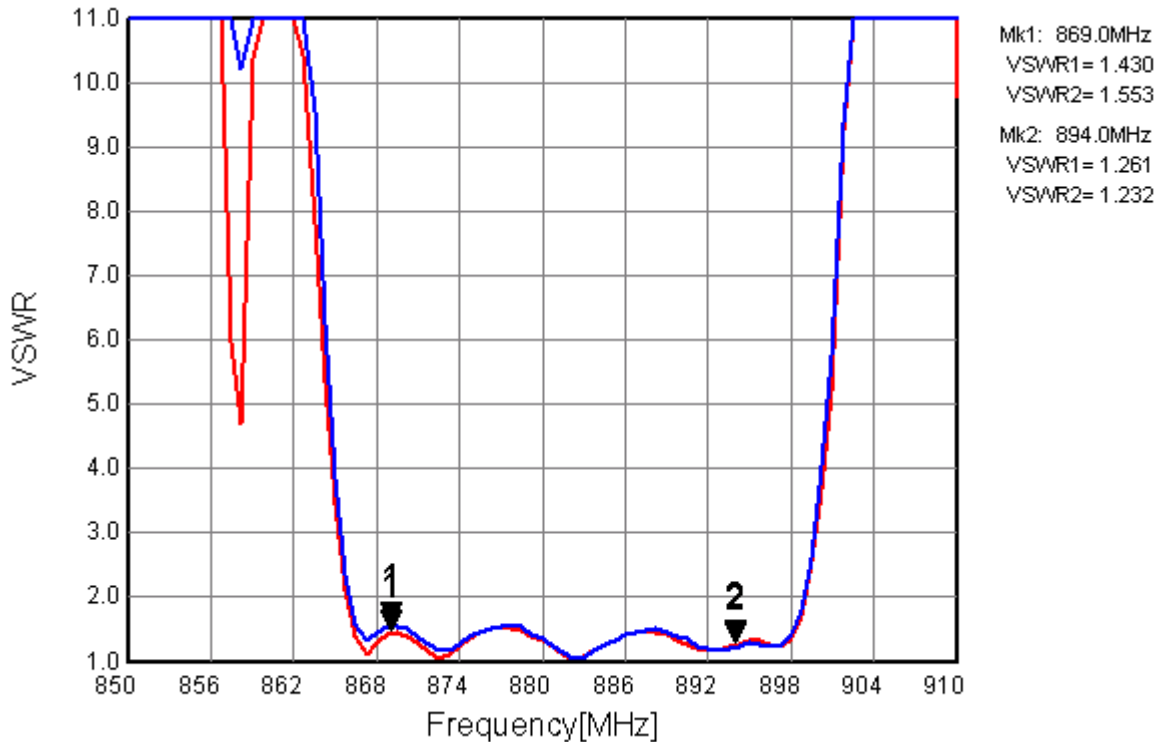
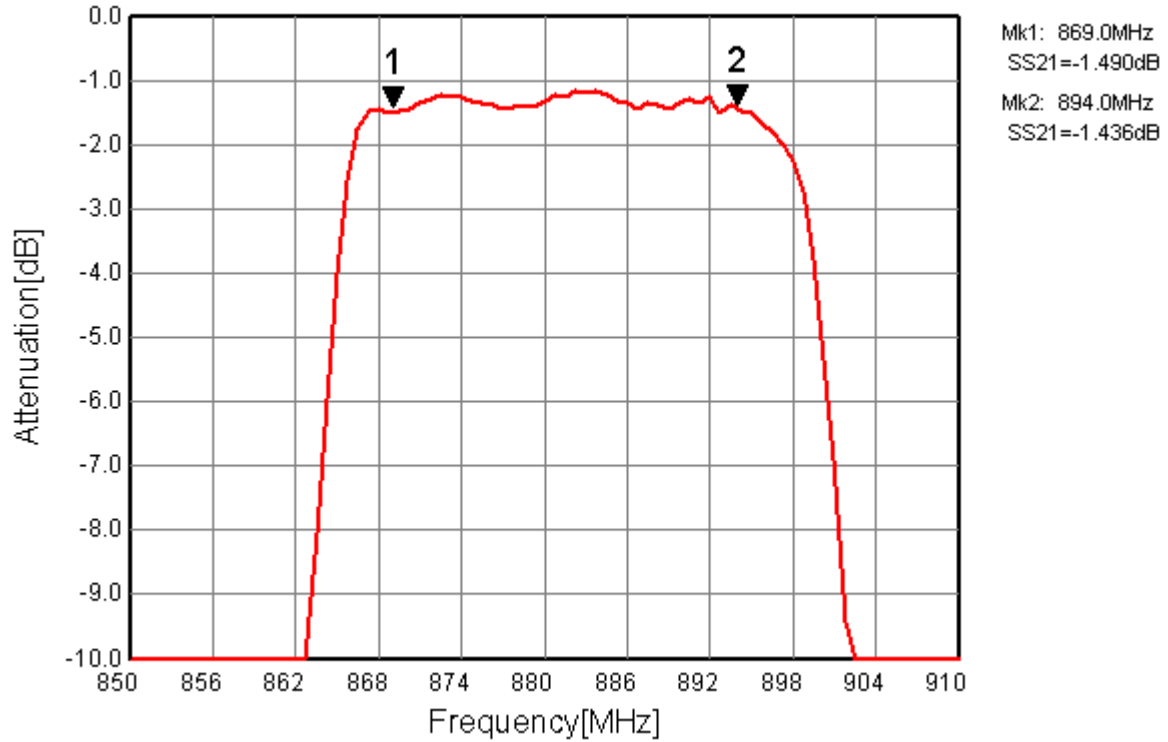


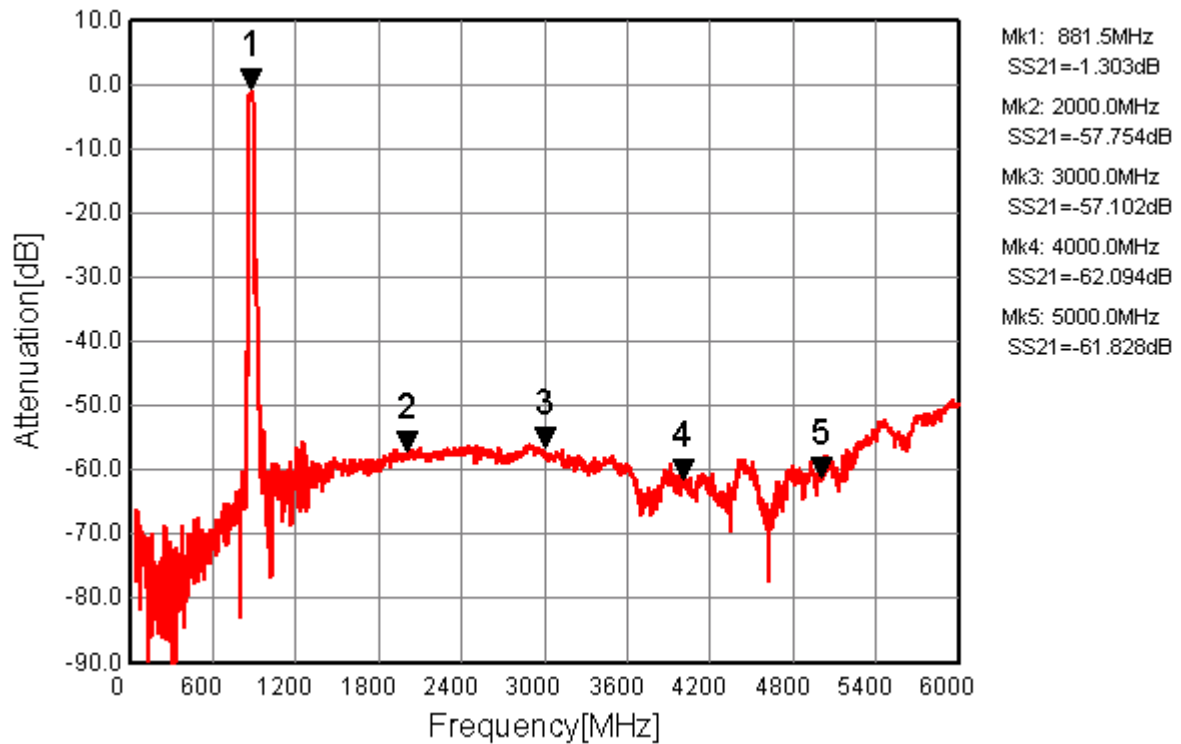
Fig.2 VSWR

— Unbal  
— Bal

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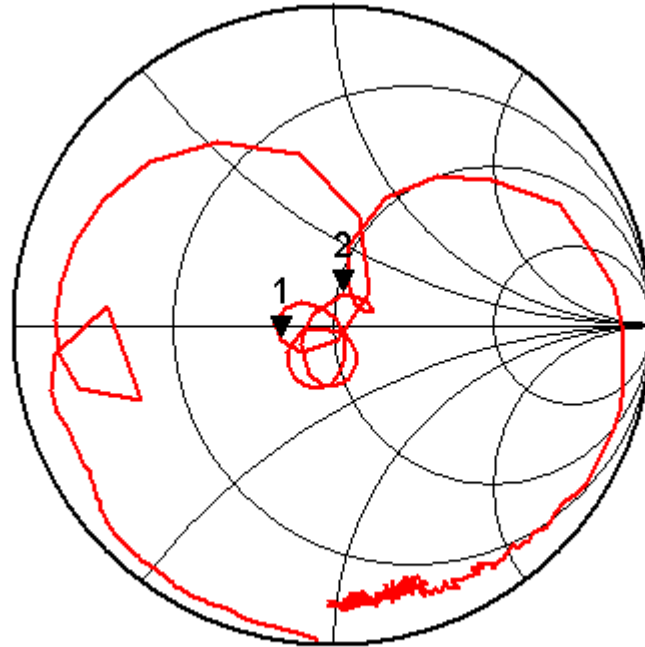


**Fig.3 In-band Characteristics**



**Fig.4 Wide-band Characteristics**

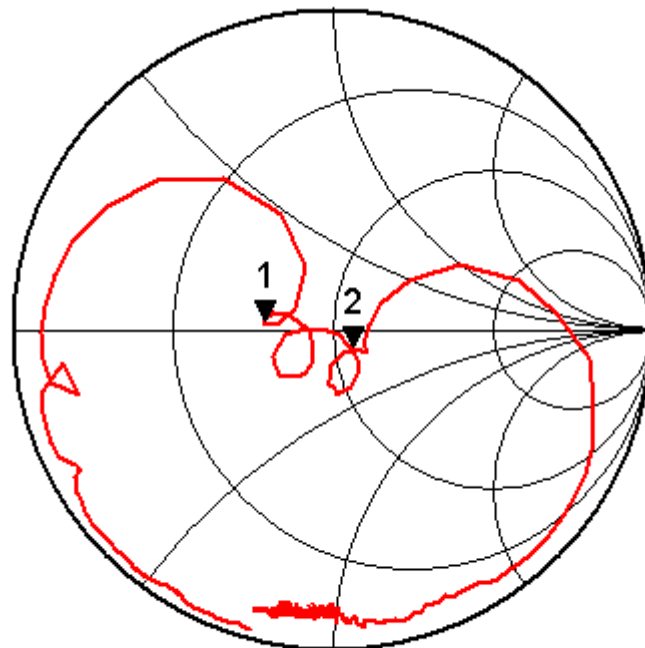
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Mk1: 869.0

Mk2: 894.0

**Fig.5 Input Impedance (Unbalance)**

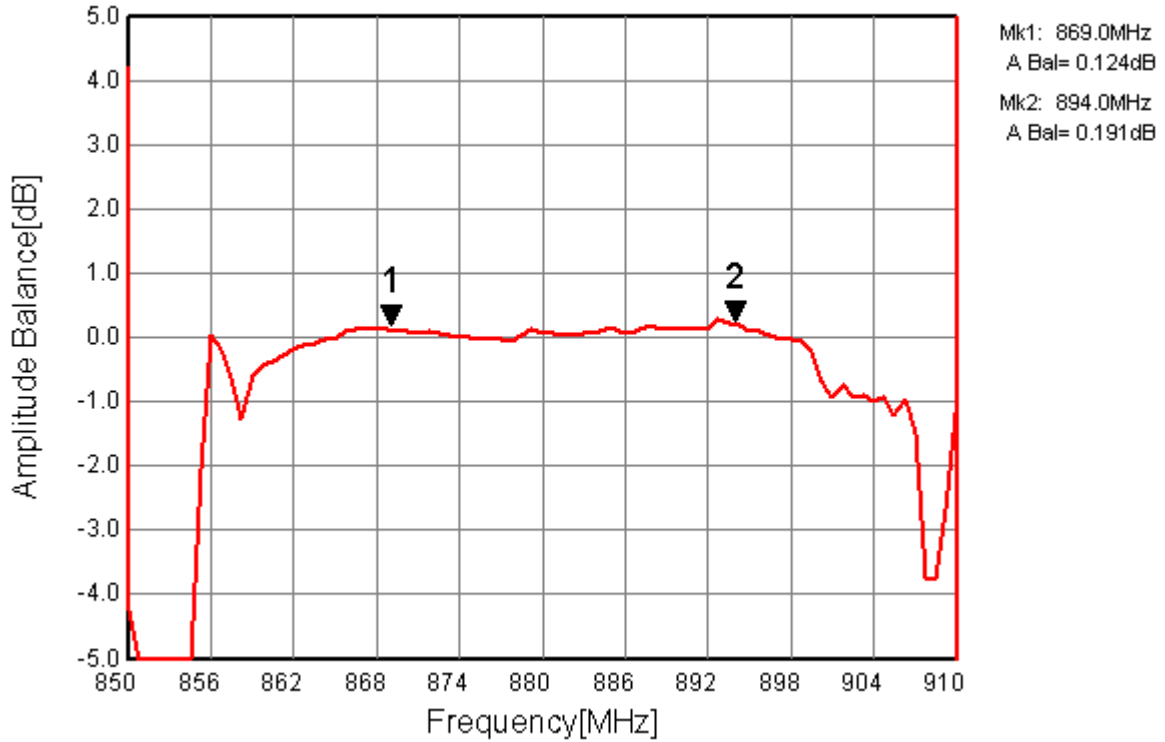


Mk1: 869.0

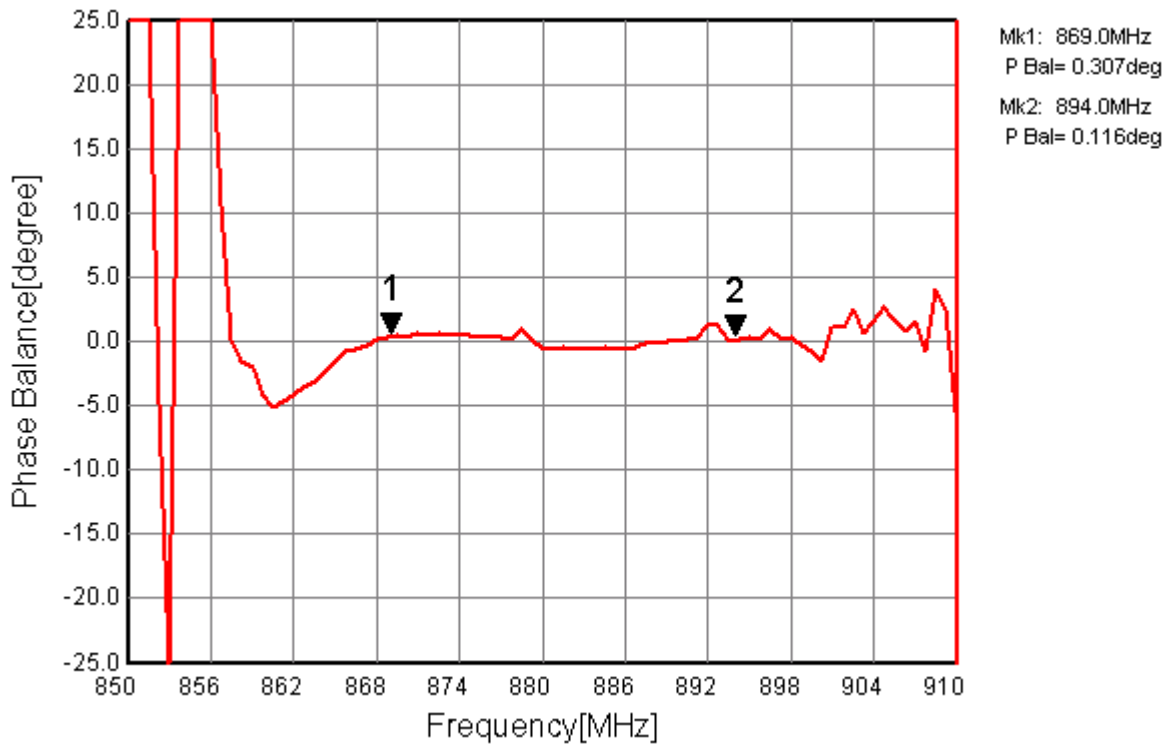
Mk2: 894.0

**Fig.6 Output Impedance (Balance)**

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**Fig.7 Amplitude Balance**



**Fig.8 Phase Balance**