

Customer Name	<b>Standard specification</b>	FUJITSU MEDIA DEVICES LIMITED	
System	JCDMA-Rx (50/100)	DATE	July. 10, 2003
FMD Part Number	FAR-F5EB-851M00-B28Y	Version 1.0d	

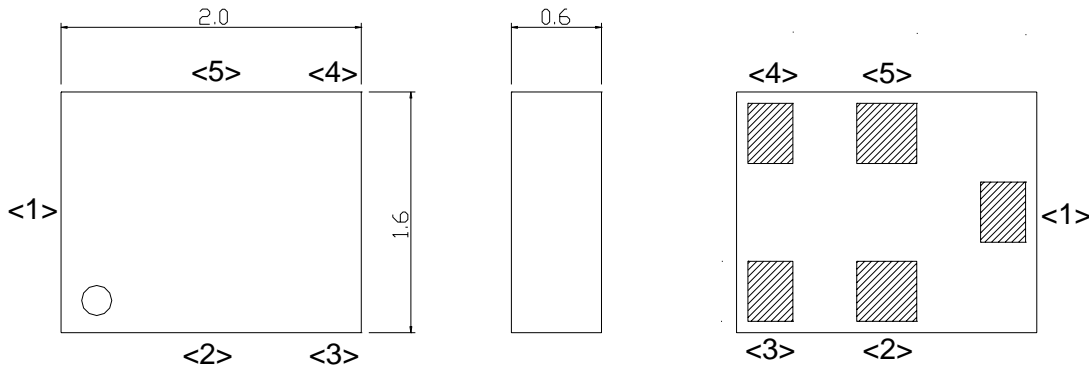
**Table 1. Electrical specifications**

Passband : 832 ~ 870 MHz						
Item	Condition	Specification			Unit	Remarks
		Min.	Typ.	Max.		
Insertion Loss	832~870 MHz	-	2.4	3.2	dB	
Ripple	832~870 MHz	-	1.1	2.0	dB	
Absolute attenuation	DC~750 MHz	40	61	-	dB	
	750~815 MHz	25	40	-	dB	
	887~925 MHz	23	28	-	dB	
	960~3000 MHz	29	39	-	dB	
Input VSWR	Unbalance	-	2.5	2.9	-	
Output VSWR	Balance	-	2.1	2.7	-	
Amplitude balance	832~870 MHz	-1.0	-	+1.0	dB	
Phase balance	832~870 MHz	170	-	190	deg.	
Input Impedance	Unbalance	50			Ohm	
Output Impedance	Balance	100//100nH			Ohm	
Operating Temperature		-30 ~ +85			°C	
Device Size		2.0typ.x1.6typ.x0.6max.			mm	

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**Dimensions**

Device size: 2.0typ. x 1.6typ. x 0.6max

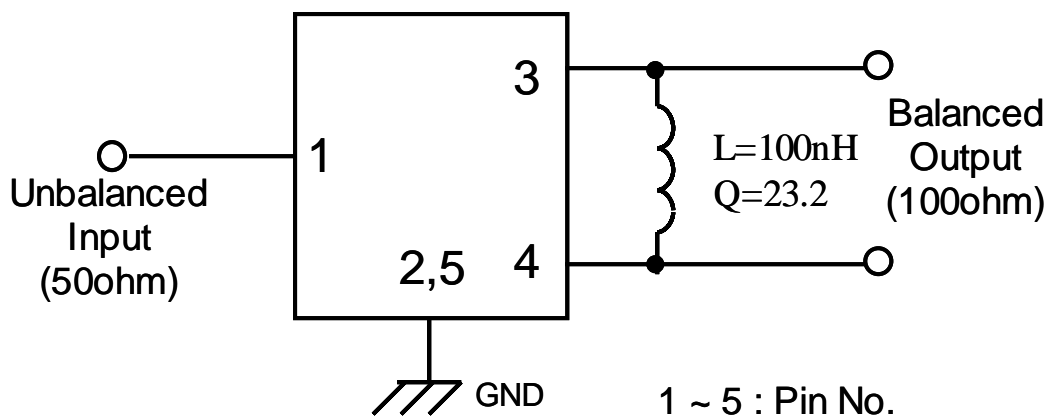


Unit: mm

● **Pin Configuration**

Pin No.	Pin name	Description
1	IN	Unbalanced pin
2	GND	Ground
3	OUT	Balanced pin
4	OUT	Balanced pin
5	GND	Ground

● **Evaluation Circuit**



Evaluation circuit

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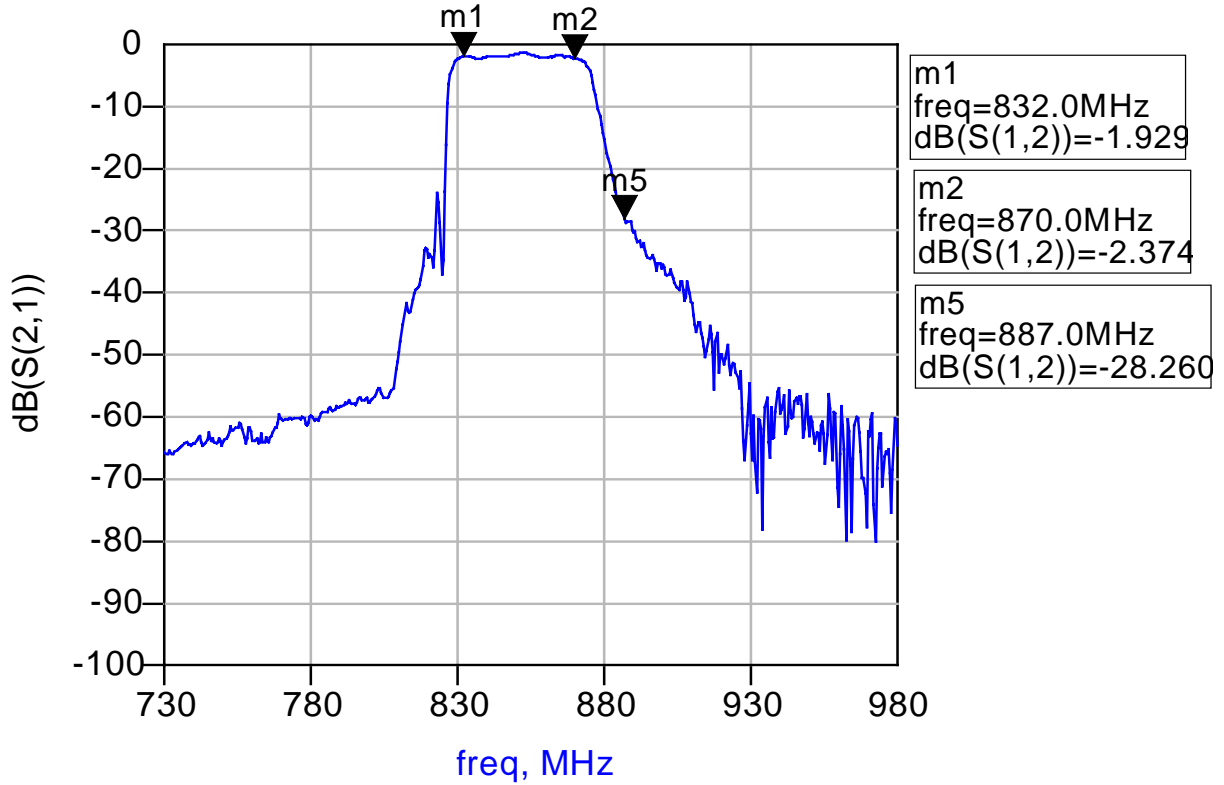


Fig. 1 Pass-band Characteristics

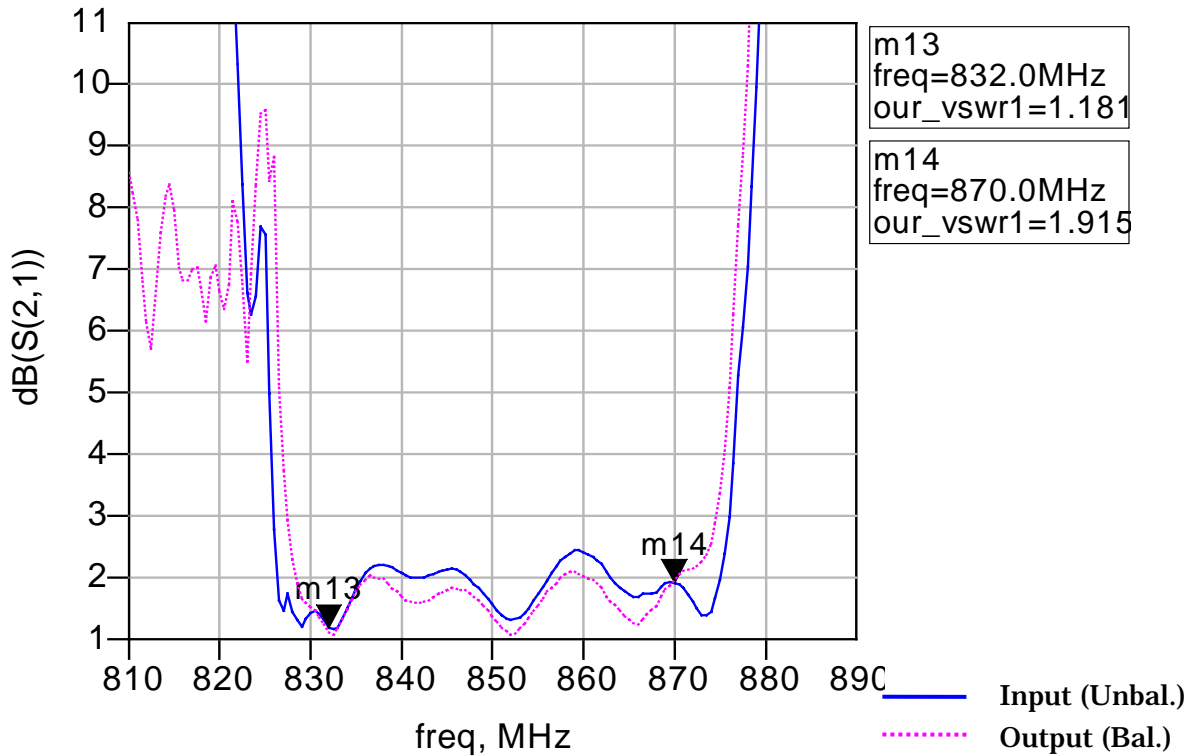
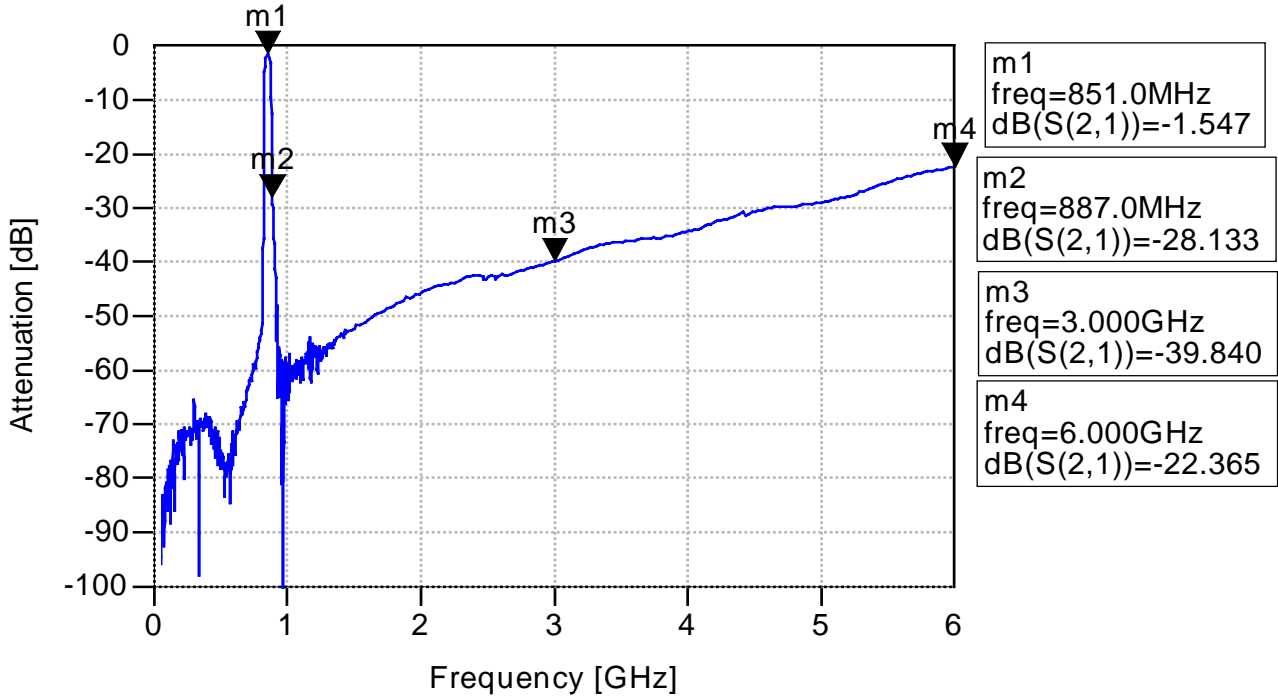
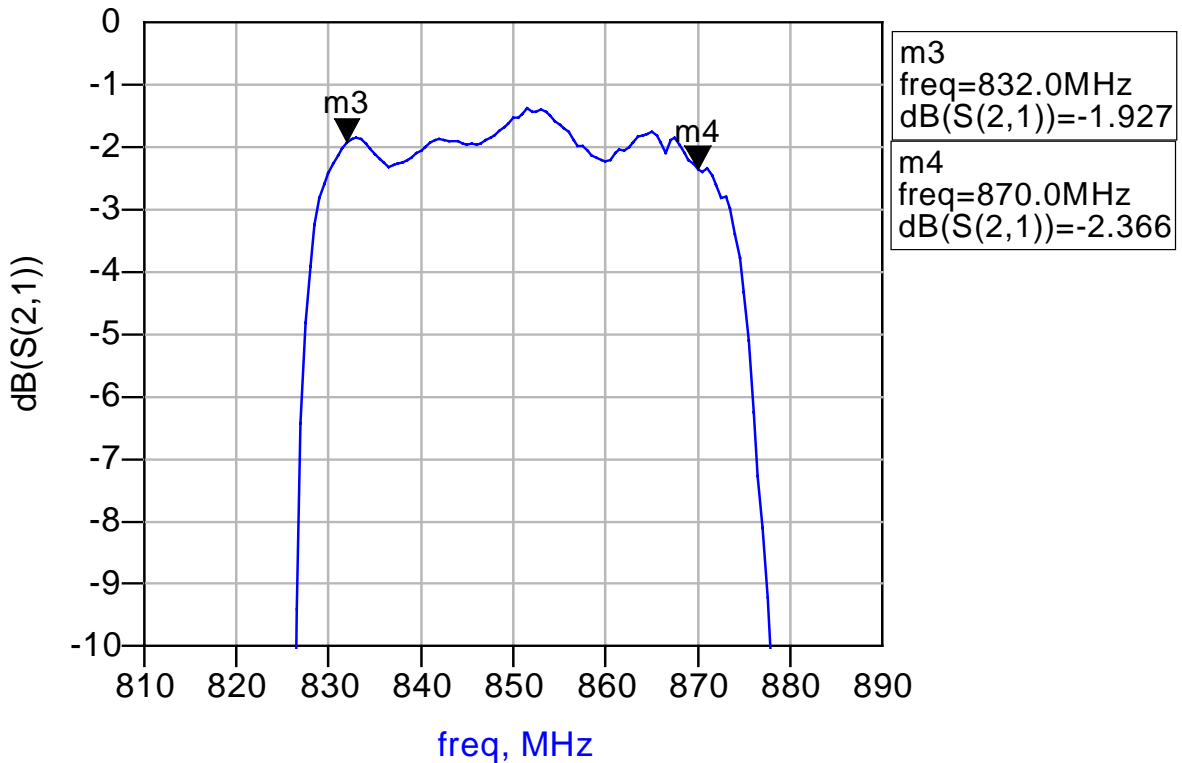


Fig. 2 VSWR

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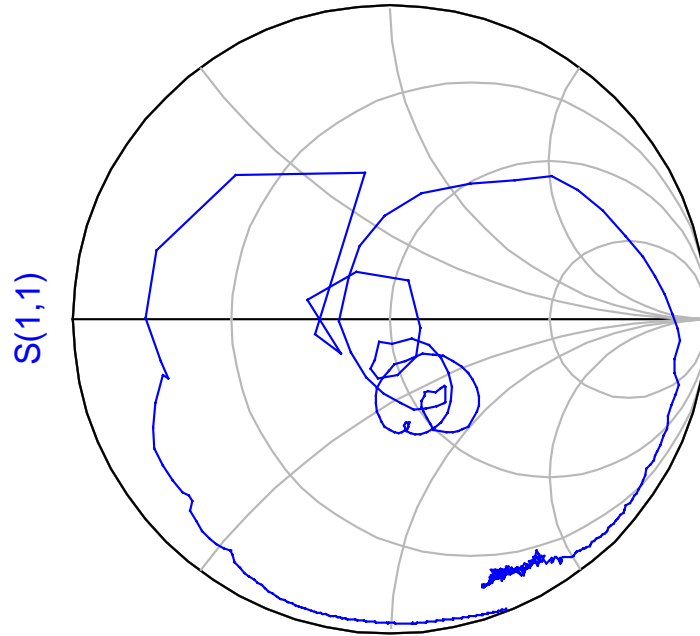
**Fig. 3 Wide-band Characteristics**



**Fig. 4 In-band Characteristics**

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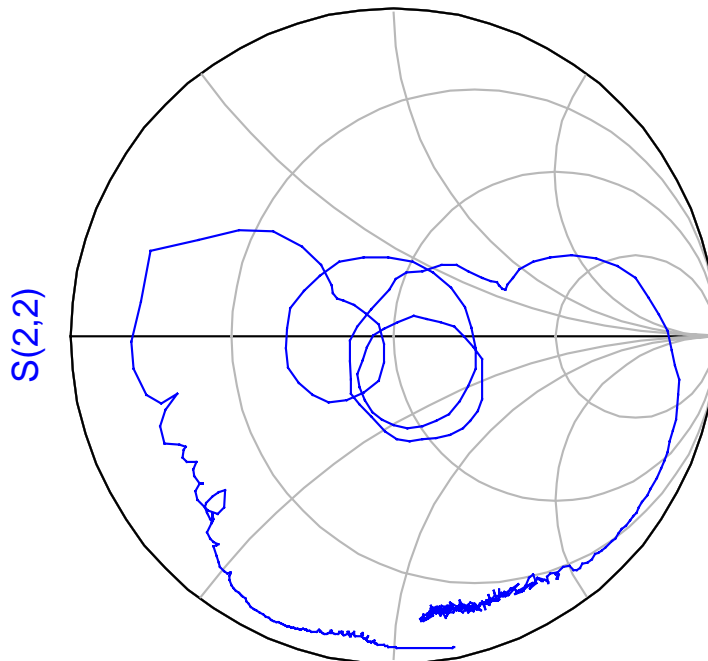
Input Reflection Coefficient



freq (700.0MHz to 1.000GHz)

**Fig. 5 Input Impedance (Unbal.)**

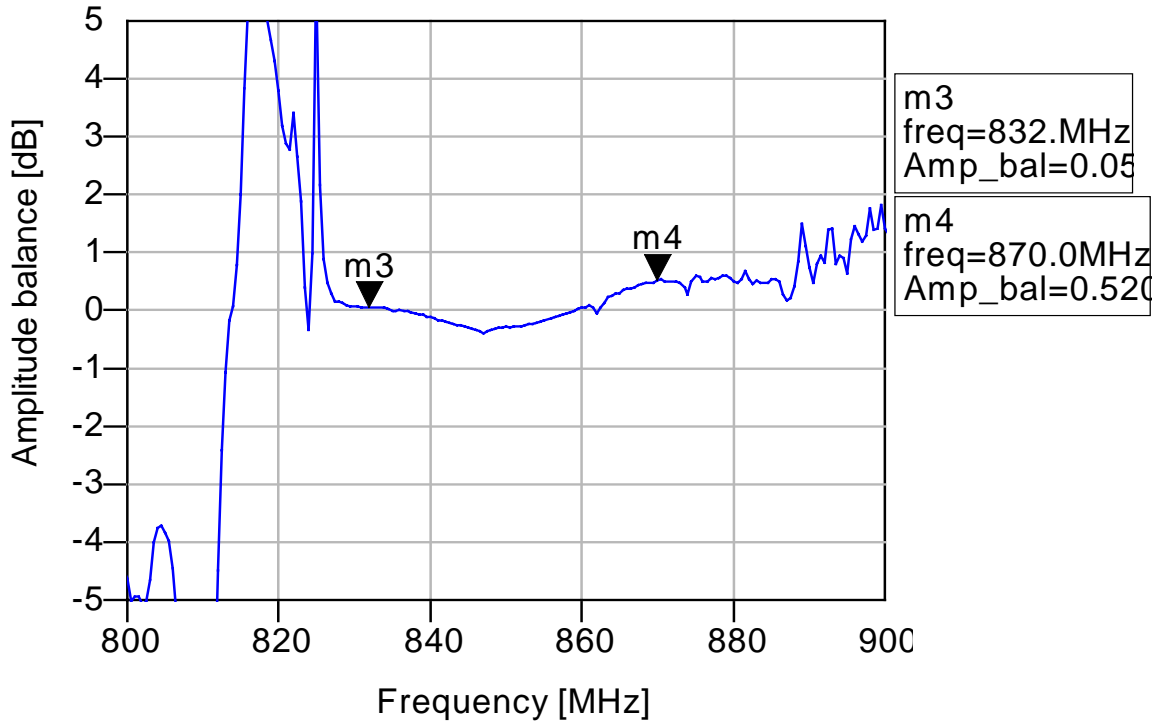
Output Reflection Coefficient



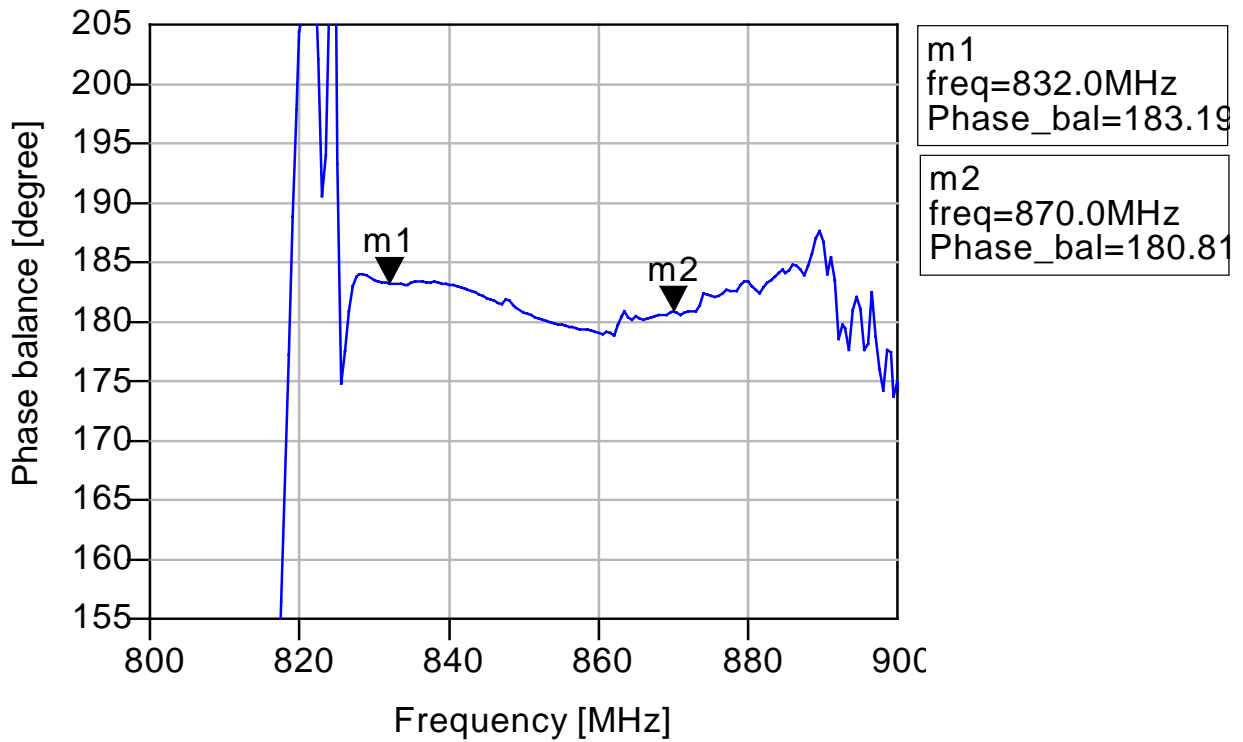
freq (700.0MHz to 1.000GHz)

**Fig. 6 Output Impedance (Bal.)**

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**Fig. 7 Amplitude Balance**



**Fig. 8 Phase Balance**